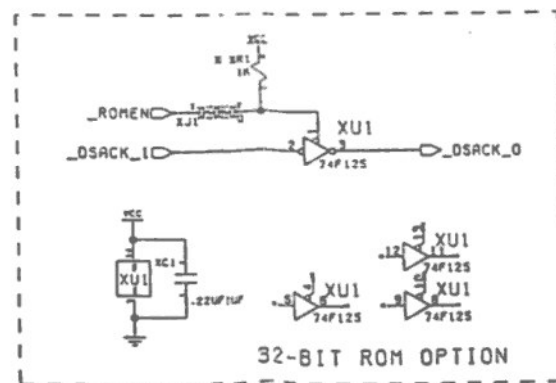
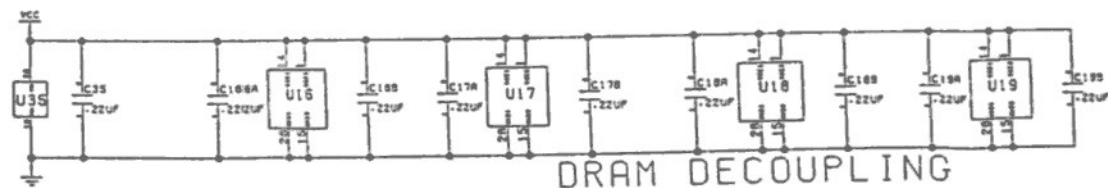
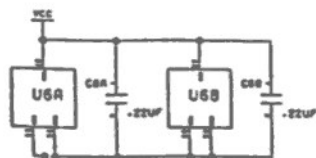
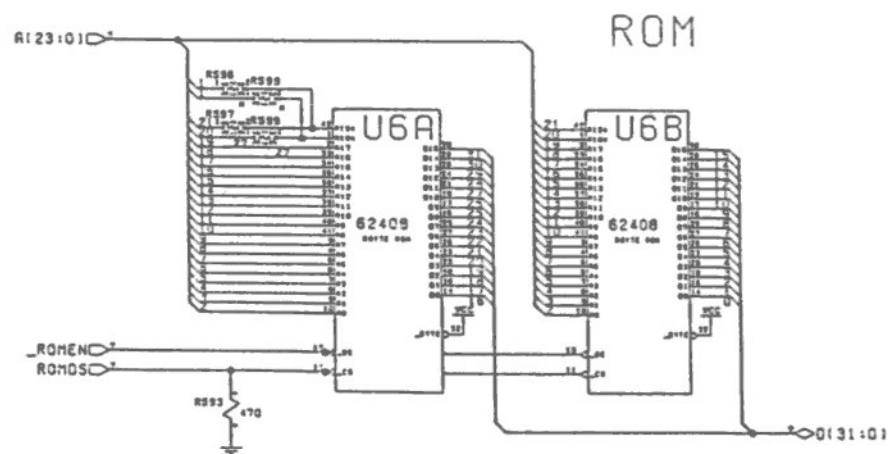
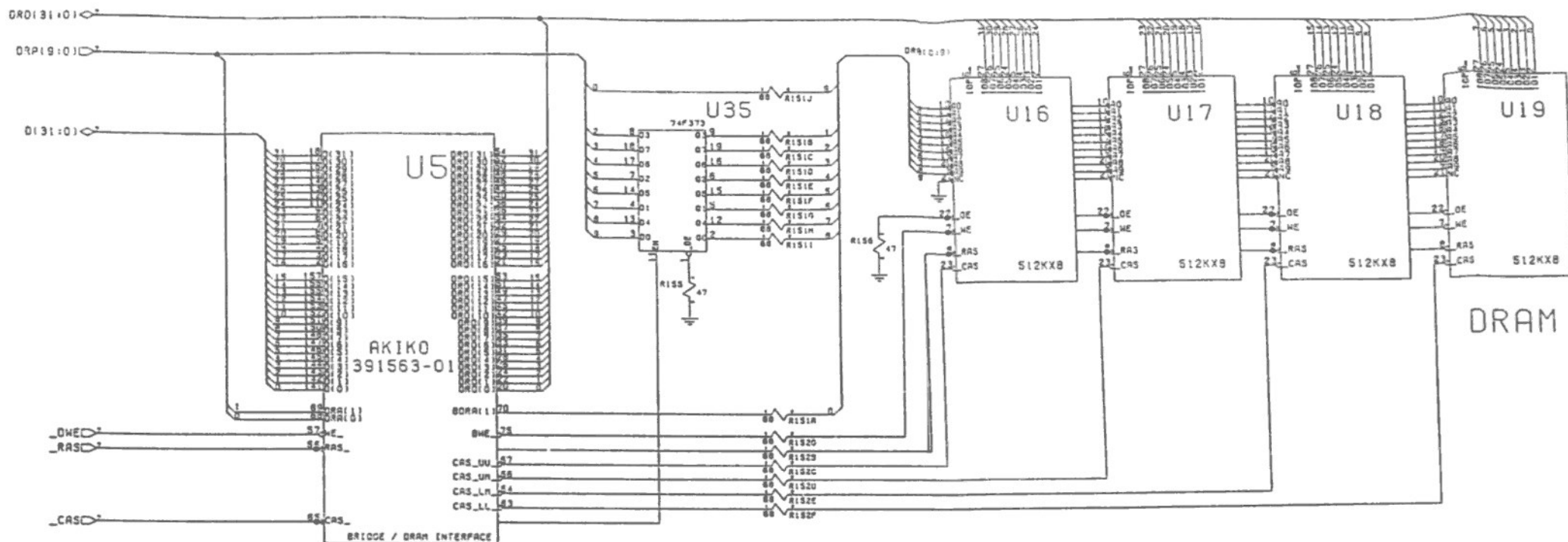


SIGNAL GLOSSARY

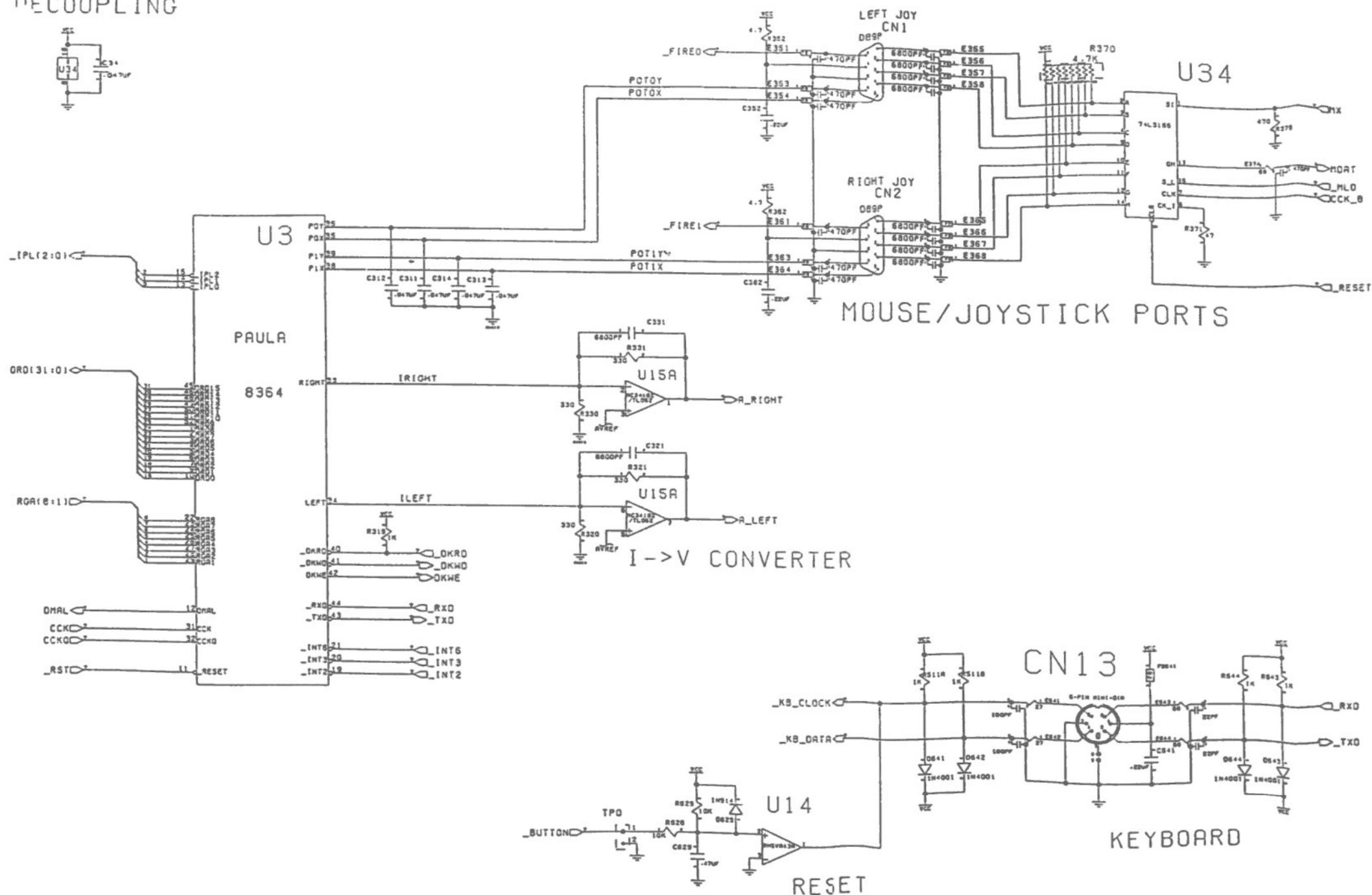
SIGNAL	DESCRIPTION (AREA)	PAGES
20MHz	20.53536 MHz MASTER CLOCK	
20MHz	7.15509 MHz PROCESSOR CLOCK	
A(23.1)	PROCESSOR ADDRESS BUS (60000)	
ALC	DATA ACKNOWLEDGE (PARALLEL PORT)	
AS	ADDRESS STROBE (60000)	
AUDIN	AUDIO INPUT (RS232 PORT)	
AUDOUT	AUDIO OUTPUT (RS232 JACK)	
BEK	BUS BEAK (60000)	
BS	BUS STALL (60000)	
BSCLK	BUS STALL ACKNOWLEDGE (60000)	
CLISS	CLOCK SIGNAL (CHIPS)	
CLL	CHIP SELECT ACCESS (CHIPS)	
CS	BUS REQUEST (60000)	
DUS1	DEVICE BUS (PARALLEL PORT)	
DATA/CLK	COLUMN ADDRESS STROBE (URAH)	
CLK/CLK	CLOCK / QUADRATURE (CHIPS)	
CLK	7.15509 MHz QUADRATURE CLOCK (CHIPS)	
CHRG	CHARGE (FLOPPY)	
CLK/CLK	READ-TIME CLOCK READ / WRITE (RICE)	
COMP	COMPOSITE SYNC (VIDEO)	
CSync	COMPOSITE SYNC (VIDEO)	
CIS	CLEAR TO SEND (RS232 PORT)	
D(15.0)	PROCESSOR DATA BUS (60000)	
DIA	STEP DIRECTION (FLOPPY)	
DRD	DATA READ DATA (FLOPPY)	
DRND	DATA WRITE DATA (FLOPPY)	
DRWL	DATA WRITE ENABLE (FLOPPY)	
DRHL	DATA READ REQUEST LINE (CHIPS)	
DRH(0.0)	DATA ADDRESS BUS (URAH)	
DRH(15.0)	DATA DATA BUS (URAH)	
DRS	DATA SET READY (RS232 PORT)	
DRCK	DATA TRANSFER ACKNOWLEDGE (60000)	
DR	DATA TERMINAL READY (RS232 PORT)	
C	PERIPHERAL ENABLE CLOCK (60000)	
EXCLK	EXPANSION PRESENT / RICE TICK	
FL(2.0)	FUNCTION CODE (60000)	
FL(0.0)	FIRE BUTTON (JOYSTICKS)	
HL	PROCESSOR HOLD (60000)	
HSync	HORIZONTAL SYNC (VIDEO)	
INDEX	INDEX PULSE (FLOPPY)	
INT(2.3.6)	INTERLUPT REQUEST (CHIPS)	
IRRESET	I/O RESET	
IR(12.0)	INTERLUPT PRIORITY LEVEL (60000)	
KBLCLK	KEYBOARD CLOCK (KEYBOARD)	
KDATA	KEYBOARD DATA (KEYBOARD)	
KRESET	KEYBOARD RESET (KEYBOARD)	
LUS/DUS	UPPER / LOWER DATA STROBES (60000)	
LED	POWER ON LED / AUDIO TILDE MISCABLE	
LE(1.0)	LEFT RIGHT AUDIO (AUDIO)	

SIGNAL	DESCRIPTION (AREA)	PAGES
LPEN	LIGHT PEN TRIGGER (JOYSTICKS)	
DIR	DIRECTION (FLOPPY)	
DIR	DIR ON - ONLY 0 (FLOPPY)	
HOY/HOH	HOUSE 0 QUADRATURE Y/H (JOYSTICKS)	
HOY/HOH	HOUSE 1 QUADRATURE Y/H (JOYSTICKS)	
OVL	OVERLAY AND OVER RUN	
OVR	OVERLAP SYSTEM OCCURRING	
PIELSW	PIEL LOCK PIXEL SWITCH (VIDEO)	
PO(0.0)	PO LINE 0 (JOYSTICKS)	
PO(1.0)	PO LINE 1 (JOYSTICKS)	
PO(2.0)	PO LINE 2 (JOYSTICKS)	
PO(3.0)	PO LINE 3 (JOYSTICKS)	
PO(4.0)	PO LINE 4 (JOYSTICKS)	
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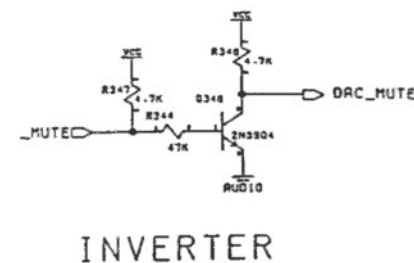
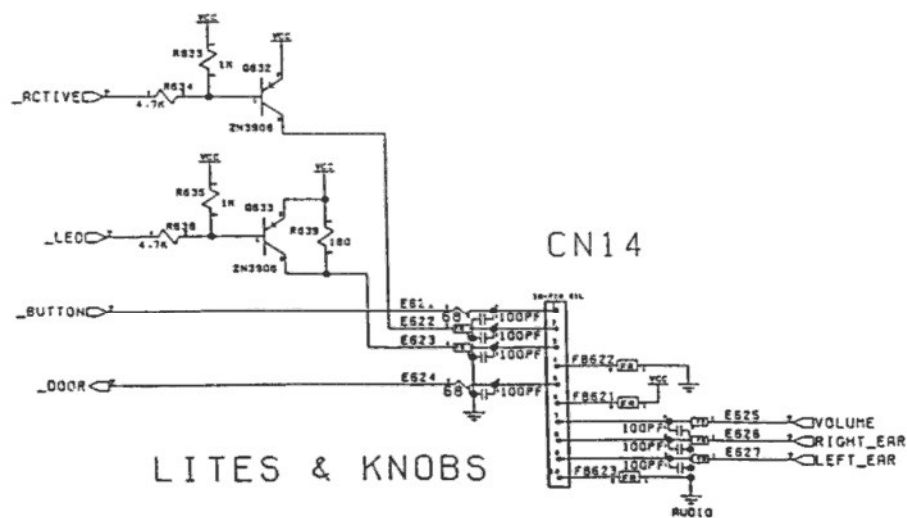
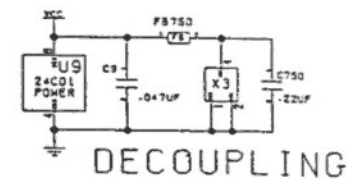
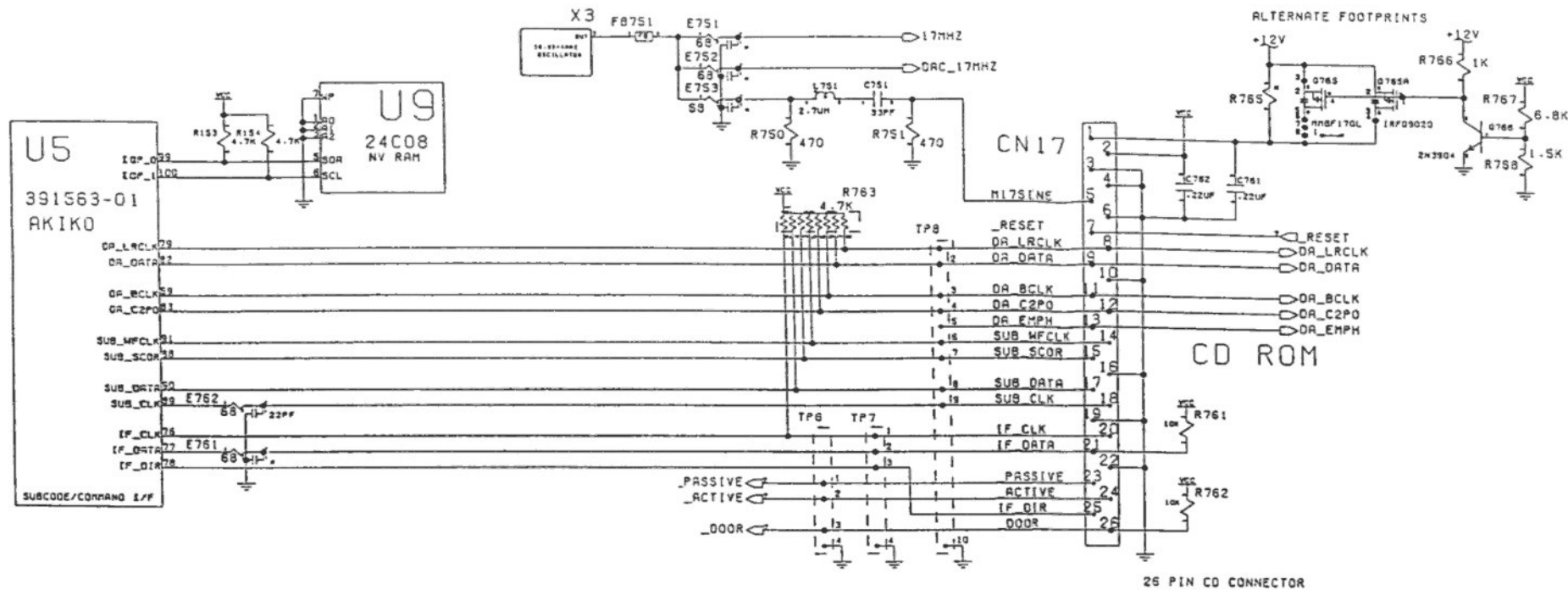


CD/GS PLAYER PCB

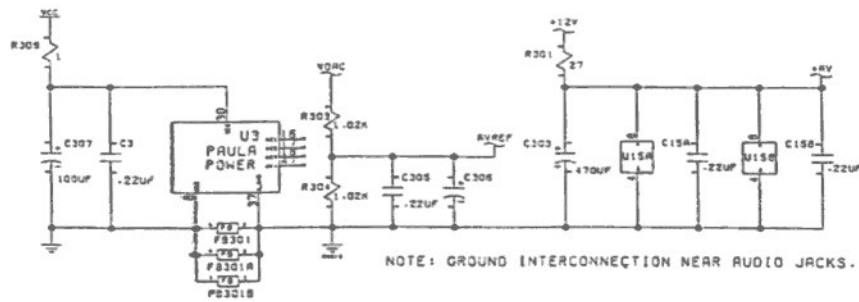
ROM DECOUPLING



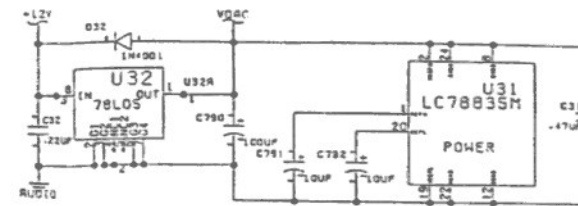
The collage consists of three distinct items. On the left is a rectangular stamp with the text 'C.A.B. DEPARTED' in a bold, sans-serif font. In the center is a sign that reads 'PALA PREFERS THE TRADITIONAL ROCKS' in a stylized, hand-drawn font. On the right is a sign with the word 'COMMODORE' in a large, bold font, and below it, the number '1365100' in a similar font.



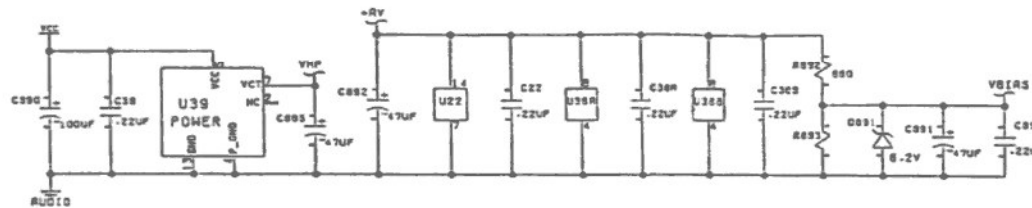
PAULA/AUDIO DECOUPLING



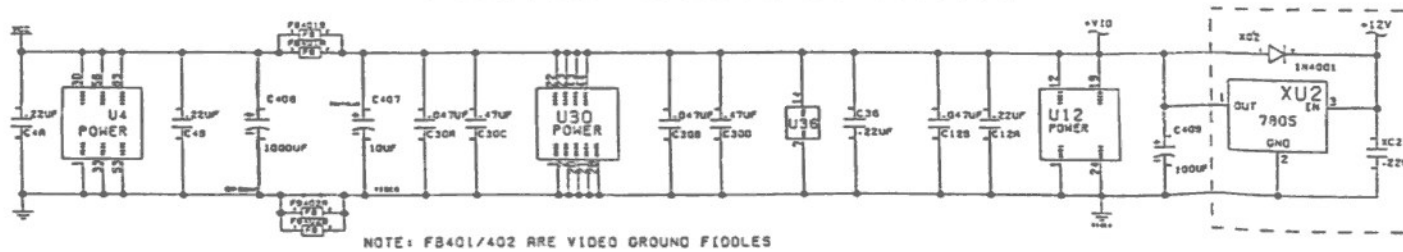
CD/AUDIO DAC DECOUPLING



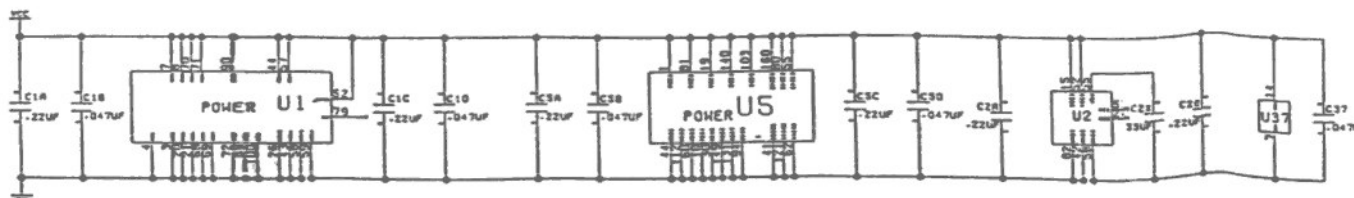
HEADPHONE/AUDIO DECOUPLING



VIDEO DECOUPLING

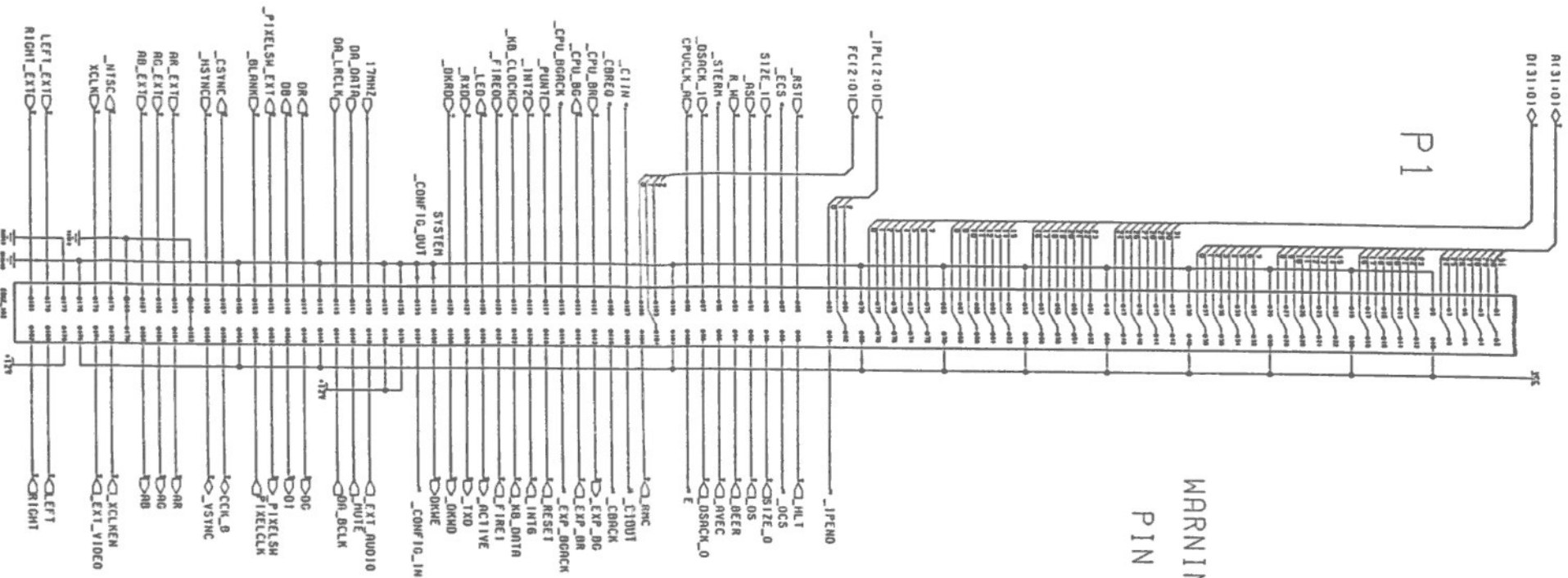


PROCESSOR/GATOR/ALICE DECOUPLING



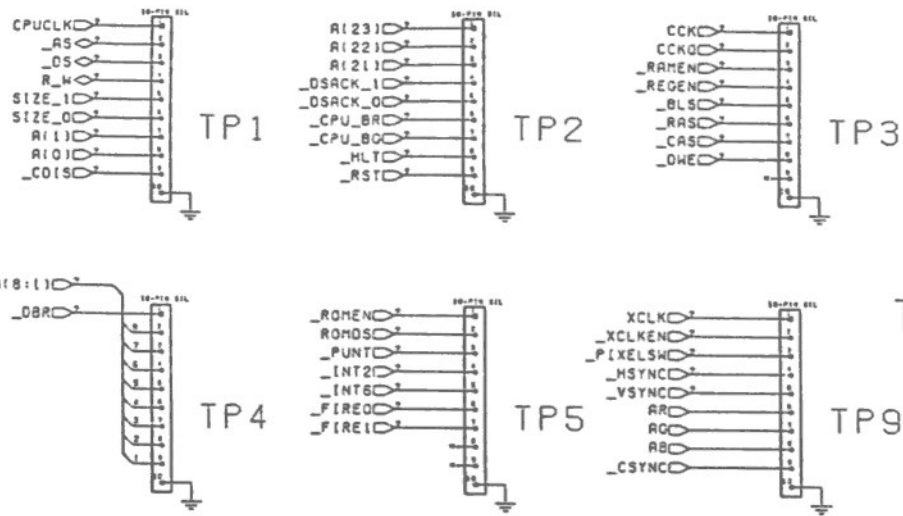
CD/GS PLAYER PCB

EXPANSION CONNECTOR



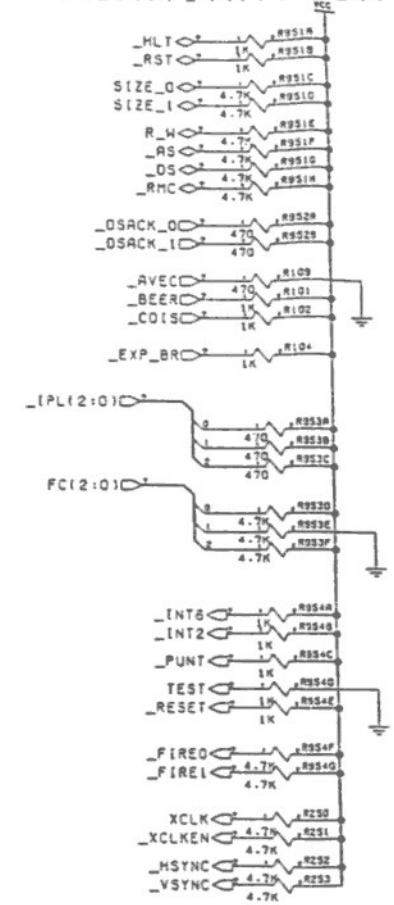
WARNING:

PIN NUMBERS ARE BACKWARDS

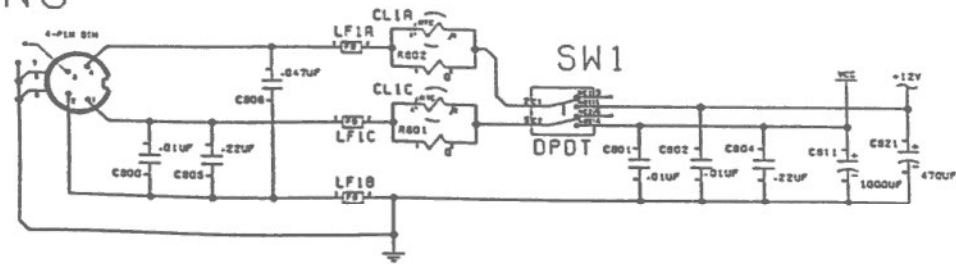


TEST ACCESS

TERMINATION



CN8 POWER INPUT



HOLES & C.

